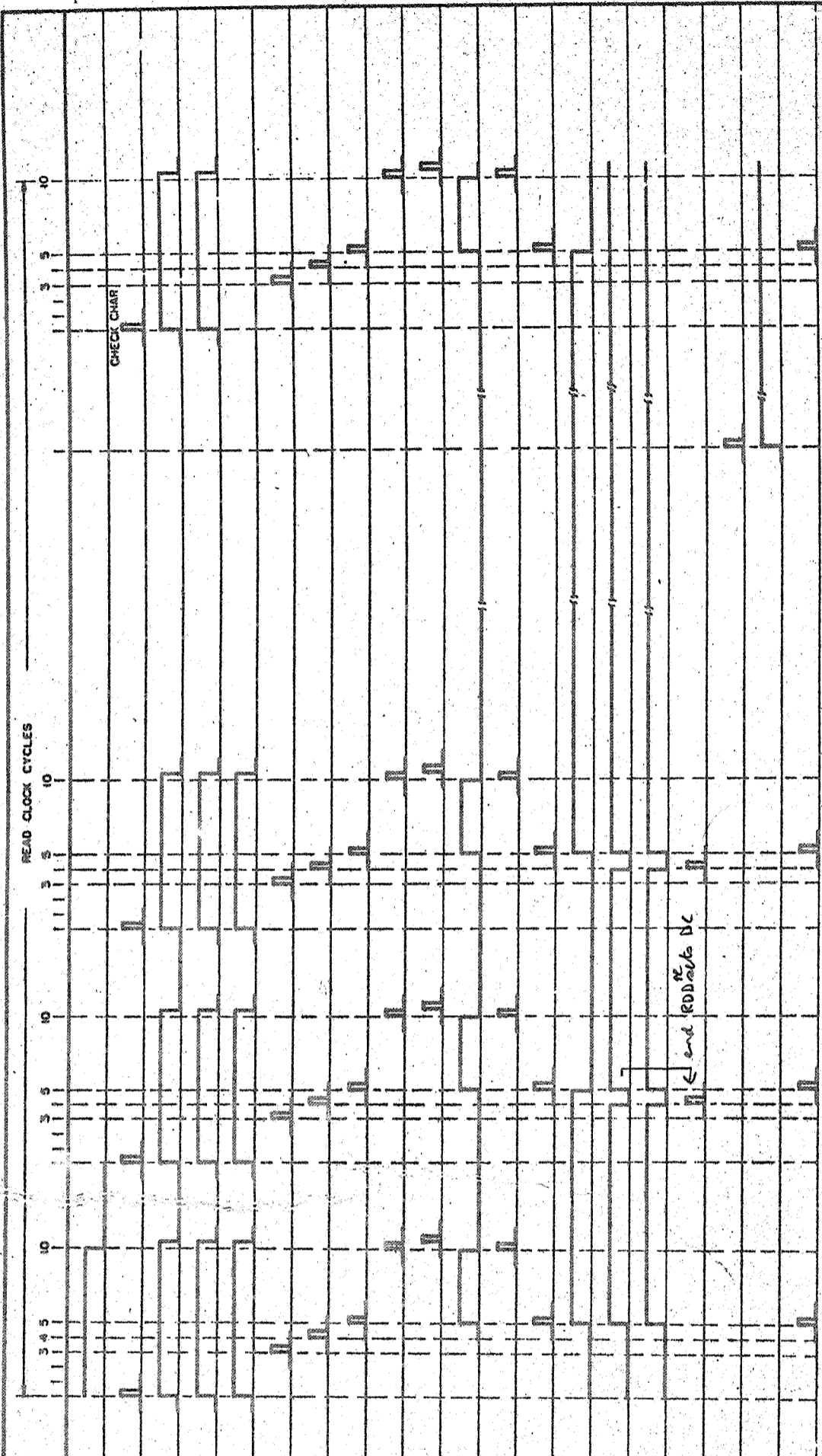


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FIG 195-3



check for this

high clip pulse -> first bit -> arrives around clock -> RC4 etc.

SIGNAL NAME	TEST POINT	LOGIC
+Y FIRST CHAR L	B9 40 60 I	
-Y HI CLIP PULSE	B9 40 60 I	
-Y FIRST BIT	B9 40 60 I	
+Y A REG 1 BIT	B9 40 20 I	
+Y B REG 1 BIT	B9 40 20 I	
-Y RC 3	B9 10 30 I	
-Y RC 4	B9 10 30 I	
-Y WR RC-5 OR READ RC-7 CNT-2	B9 10 31 I	
-Y WR RC-10 OR READ RC-7	B9 10 31 I	
+Y WR RC-10 OR READ RC-7 RESET	B9 10 31 I	
+Y SKEW GATE	B9 10 31 I	
SAMPLE COMPARE ERROR	B9 50 30 I	
SAMPLE A REG WPC	B9 50 10 I	
+Y LPCR 1 BIT	B9 50 40 I	
-Y RDD	B9 30 10 I	
+Y USEC CTRL	B9 20 10 I	
-Y DC RESET	B9 30 31 I	
+Y RDD 36	B9 30 50 I	
+Y CHECK CHAR L	B9 40 61 I	
STOP OR ERR SW ON		
SPL COMPARE ERR	B9 50 30 I	

DATE	CHANGE NO	DATE	CHANGE NO	INTERNATIONAL BUSINESS MACHINES CORP
9-7-61	1123368	7-10-61	TA 37927B	FIG 195-3 READ CLOCK CYCLES
				729 7330 WRITE OPERATION
				DESIGN TYPE
				DETAIL SCALE
				CHECK DRAW
				APPRO CHECK

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31