

INPUTS FROM 729 TAPE DRIVE

89.02.20.0

729 TP DRV LOGIC
A7 + A4
I1 + I7

TU.07.00.0	01.03.1	8VPP READ BUS 1 BIT 729	19081
TU.07.00.0	01.03.1	8VPP READ BUS 2 BIT 729	19081
TU.07.00.0	01.03.1	8VPP READ BUS 4 BIT 729	19081
TU.07.00.0	01.03.1	8VPP READ BUS 8 BIT 729	19081
TU.07.00.0	01.03.1	8VPP READ BUS A BIT 729	19081
TU.07.00.0	01.03.1	8VPP READ BUS B BIT 729	19081
TU.07.00.0	01.03.1	8VPP READ BUS C BIT 729	19081
TU.08.10.1	01.02.1	SEL + RDY WR 729	189A4
TU.08.10.1	01.02.1	SEL + RDY RD 729	189A3
TU.08.10.1	01.02.1	SEL + REWIND 729	185A2
TU.08.10.1	01.02.1	SEL + TI ON 729	183A2
TU.08.10.1	01.02.2	HIGH DENSITY 729	189A1
TU.08.10.1	01.04.1	WRITE ECHO 729	191A1
TU.08.10.1	01.06.1	SEL + LP 729	185A4
TU.08.10.1	01.06.1	SEL + NOT LP 729	189C4
TU.08.10.1	01.05.1	SEL + TI OFF 729	189C4
TU.08.10.1	01.01.1	SEL AND READY MOD 2 729	189A3
TU.08.10.1	01.01.1	SEL AND READY MOD 4-729	189A2
TU.08.10.1	01.01.1	MOD 5 OR MOD 6	187A4

OUTPUTS TO 729 TAPE DRIVE

89.02.25.0

729 TP DRV LOGIC
I1 + I7
A2 + A4

19086	WRITE BUS 1 BIT 729	01.04.1	TU.08.00.1
19086	WRITE BUS 2 BIT 729	01.04.1	TU.08.00.1
19086	WRITE BUS 4 BIT 729	01.04.1	TU.08.00.1
19086	WRITE BUS 8 BIT 729	01.04.1	TU.08.00.1
19086	WRITE BUS A BIT 729	01.04.1	TU.08.00.1
19086	WRITE BUS B BIT 729	01.04.1	TU.08.00.1
19085	WRITE BUS C BIT 729	01.04.1	TU.08.00.1
18202	WRITE PULSE 729	01.04.1	TU.08.00.1
18206	WR CK CH 729	01.04.1	TU.08.00.1
181C5	SET WR STATUS 729	01.02.1	TU.08.00.1
183C6	SET READ STATUS 729	01.02.1	TU.08.00.1
185C5	BACKWARD 729	01.07.1	TU.08.00.1
189C6	GO 729	01.07.1	TU.08.00.1
185B2	REWIND 729	01.09.1	TU.08.00.1
185J2	REWIND UNLOAD 729	01.09.1	TU.08.00.1
189D3	TURN OFF TI 729	01.05.1	TU.08.00.1
183D2	TURN ON TI 729	01.05.1	TU.08.00.1

INPUTS FROM 7330 TAPE DRIVE

89.02.21.0

7330 TP DRV LOGIC

73.30.05.1	8VPP READ BUS 1 BIT 7330	19081
73.30.05.1	8VPP READ BUS 2 BIT 7330	19081
73.30.05.1	8VPP READ BUS 4 BIT 7330	19081
73.30.05.1	8VPP READ BUS 8 BIT 7330	19081
73.30.05.1	8VPP READ BUS A BIT 7330	19081
73.30.05.1	8VPP READ BUS B BIT 7330	19081
73.20.05.1	8VPP READ BUS C BIT 7330	19081
73.20.25.1	SEL + RDY WR 7330	189A4
73.20.25.1	SEL + RDY RD 7330	189A3
73.20.25.1	SEL + REWIND 7330	185A1
73.20.25.1	SEL + TI ON 7330	183A1
73.20.25.1	SEL + RDY HI 7330	189A2
73.20.05.1	ECHO PULSE 7330	191A1
73.20.25.1	SEL + LP 7330	185A4
73.20.25.1	SEL + RDY LO 7330	189A2
73.20.25.1	SEL RDY BWD 7330	185C3

OUTPUTS TO 7330 TAPE DRIVE

89.02.26.0

7330 TP DRV LOGIC

19086	WRITE BUS 1 BIT 7330	73.30.05.1
19086	WRITE BUS 2 BIT 7330	73.30.05.1
19086	WRITE BUS 4 BIT 7330	73.30.05.1
19086	WRITE BUS 8 BIT 7330	73.30.05.1
19086	WRITE BUS A BIT 7330	73.30.05.1
19086	WRITE BUS B BIT 7330	73.30.05.1
190C6	WRITE BUS C BIT 7330	73.30.05.1
182D2	WRITE PULSE 7330	73.30.05.1
182D5	WR TR REL 7330	73.30.05.1
181C5	SET WRITE STATUS 7330	73.20.25.1
183C6	SET READ STATUS 7330	73.20.25.1
185C5	BACKWARD 7330	73.20.25.1
189C6	GO 7330	73.20.25.1
185B2	REWIND 7330	73.20.25.1
185D2	REWIND UNLOAD 7330	73.20.25.1
189D3	TURN OFF TI 7330	73.20.25.1
183D2	TURN ON TI 7330	73.20.25.1

INPUTS TO TAU

89.02.30.0

1401 ILD

129D3	1 BIT WRITE DATA LINE	190A4
129D3	2 BIT WRITE DATA LINE	190A4
129D3	4 BIT WRITE DATA LINE	190A4
126D4	8 BIT WRITE DATA LINE	190A4
126D4	A BIT WRITE DATA LINE	190A4
126D4	B BIT WRITE DATA LINE	190A4
129D5	C BIT WRITE DATA LINE	190A4
130D2	WR CALL	181A5
130D2	WR TH CALL	181A6
129B3	DISC CALL	182A5
126C4	READ CALL	183A6
126B3	REWIND CALL	185A2
126B3	REWIND + UNLOAD CALL	185B2
126B2	BACKSPACE CALL	185A6
126B2	BACKSPACE CALL	192A1
129C3	TAU RESET	186A1
127C5	ODD REDUNDANCY CALL	183A3
129D4	TURN OFF TI	189C3
130B5	MAN WR DISC	181A4
130D4	MANUAL STOP ON ERROR	192B5
130D4	MANUAL STOP ON ERROR	192C2
	EARLY SAMPLE	192A3
	EARLY SAMPLE	191C4
	AMPLIFIER BIAS	190A1
	COMPARE CHECK CE	192B5
	REG A ONLY	191B6
	REG B ONLY	191B6
130C3	TURN ON GO	189B5
	NOT MANUAL OP	181A5
	SET TAU ERROR LATCH	192A2
126B2	ERASE CALL	185A5
	DENSITY SW 200 + 556	187A3
	DENSITY SW 200 + 556	187A4
	DENSITY SW 200 + 800	187A3
	DENSITY SW 556 + 800	187A3
	DENSITY SW 556 + 800	187A2

OUTPUTS FROM TAU

89.02.35.0

1401 ILD

190A6	R-W REG 1 BIT	127C3
190A6	R-W REG 2 BIT	127C3
190A6	R-W REG 4 BIT	127C3
190A6	R-W REG 8 BIT	127C4
190A6	R-W REG A BIT	127C4
190A6	R-W REG B BIT	127C4
190A6	R-W REG C BIT	127B5
183D4	END READ DELAY	128A2
182C6	WRITE COND	
181D5	BUSY	130A4
185B4	LOAD POINT	126B2
188D2	CHECK CHAR	130A3
183B1	SEL + TI ON	130A4
192C2	ERROR	129B3
188B3	FWD STOP DELAY	129A5
182D3	WC 2	126A2
182C2	WC 8 TR	130A2
184B4	WR RC-5 OR READ RC-7	126A1
181C4	WD 52	130A5
181C3	WD 72	130A5
181C3	WD 320 OR WD 1088	130A5
181C4	WD 49	128A2
189C2	SEL + READY MOD 4	128A2
189B2	SEL + RDY HI 7330	128B2
186B1	729 HIGH DENSITY 800 OR 556 BPI	128A2
184C2	RC 4 TR	128B2
181B6	WR TH DRIVE	126A2
185B1	TAU READY	

DATE	CHANGE NO	DATE	CHANGE NO	INTERNATIONAL BUSINESS MACHINES CORP
17.10.61	124688	17.10.61	TA 37927 B	FIG 180 REFERENCE SHEET
7-6-65	124688	18.OKT65	TA 32501 F	
DESIGN		TYPE		
DETAIL		SCALE		
CHECK		DRAW		
APPRO		CHECK		

723750