

COPY

September 29, 1961

Memorandum to: Mr. C. W. Allen
Subject: 1401 Compatibility Within Family

Both Mr. Scott and I have discussed with you the problems that are being reported to us with respect to the Engineering Change level of 1401's and the lack of compatibility in a customer's office within the 1401 family. There are the problems of maintaining the 1401 Engineering Change level up to date so that machines are compatible, the problem of compatibility between machines with differing features on programs where the differing features are not used, and the problems of compatibility when special engineering features are on the machine.

Would you please make sure that this is thoroughly investigated and give me a report within thirty days on the situation that we have and the program that we have for being sure that this whole area is under control.

/s/ J. W. Franstra

JWH:cag

cc: Mr. O. M. Scott

October 12, 1961
280

Subject and/or
Reference:

1401 Compatibility
Mr. Haanstra's Memo September 29

To:

Mr. K. A. Bell
Mr. J. S. Conzola
Mr. A. D. Miller
Mr. J. L. Pokoski
Mr. W. S. Schaffer

As you know, there have been many problems and much controversy over compatibility of 1401's with each other. As preparation for the report which Mr. Haanstra has requested, I would like to have a list prepared of all items that you can think of whose operation varies depending upon the presence or absence of an optional feature, and concurrently, Mr. Pokoski should complete the list of Engineering Changes which have affected programming.

Please try to complete these lists in time for a meeting to review them Tuesday morning, October 17.

J. J. Ingram
J. J. Ingram

JJI/ka

October 16, 1961
260

1401 Compatibility

Mr. J. J. Ingram

Attached is a list of Engineering Changes which functionally affect 1401 operation. Stage I changes are covered up to E. C. 112227U - F. B. 485643. Stage II changes include all changes to E. C. 110284R - F. B. 485643.

Engineering Changes which corrected close timing conditions which could lead to a functional machine difference were omitted, since most changes are reliability changes of this type. Also, changes which correct a false error condition (excluding Address Validity Checks) were not considered.

In addition to the EVL's mentioned, there have been several no charge RPC's released which affect Stage I - Stage II compatibility:

1. RPC 494465 disables the zone bit check at I 3 or I 6 time.
2. RPC 494466 disables the indexing invalid address check.
3. RPC 896053 allows short addresses.

John Pokoski
Associate Engineer

JJ/ka
cc: Mr. K. A. Bell
Mr. J. S. Conzola