

Date: November 2, 1961  
From: Department 391

Subject: 1401 Sync points on the C. E. Panel  
Ref.: Our phone conversation of October 31, 1961

To: Mr. W. Mizer

The following changes are being made to the C. E. Panel as a result of the Product Testing "B" Test evaluation of the Overlap feature on the 1401.

<u>Old</u>	<u>New</u>
DIODE	✗ U IN
DIODE (top row)	✗ U IN
DIODE (lower row)	— T OUT
T - O	✗ U TO
ADDR STOP	✗ U ADDR STOP
READ SCAN	✗ U READ SCAN
X GATE	✗ U READ FEED
NOT PR X FER COMP	✗ U NOT PR X FER COMP
DIODE (top row)	✗ U START LATCH
DIODE	✗ U DELT PROC
DIODE (lower row)	✗ U DELT OVLV
S & R SS	✗ U PROC CHECK
Y GATE	✗ U PCH FEED
PCH SCAN	✗ U PCH SCAN
NOT 1st SCAN	✗ U NOT 1st SCAN
STR LATCH	— T STOP CLOCK

This change has had the verbal approval of Product Testing and Mr. C. Ross of Reliability and Serviceability has seen the proposed change and has given R & S approval and recommendations. Mr. Ross recommended that the inputs to the "and" circuit and the "— T Stop" circuit be biased to allow proper machine operation while not in use.

This change must be implemented before the end of C test, therefore, action is being taken to rework parts involved and prepare the necessary changes to the 1401 circuits. Please inform us immediately of any objections in this area.

*O. H. Smith*

O. H. Smith  
Project Engineer  
1401 and 650 Systems Engineering

OHS:ak

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