

(INDICATORS) \*

TAU LOGIC	NAME	#2A1 LOGIC
89.30.40.1	DC 1 TR IND	71.81.11.2
89.30.40.1	DC 2 TR IND	71.81.11.2
89.30.40.1	DC 4 TR IND	71.81.11.2
89.30.41.1	DC 8 TR IND	71.81.11.2
89.30.42.1	DC 16 TR IND	71.81.11.2
89.30.42.1	DC 32 TR IND	71.81.11.2
89.30.42.1	DC 64 TR IND	71.81.11.2
89.30.42.1	DC 128 TR IND	71.81.11.2
89.30.43.1	DC 256 TR IND	71.81.11.2
89.30.43.1	DC 512 TR IND	71.81.11.2
89.30.43.1	DC 1024 TR IND	71.81.11.2
89.30.43.1	DC 2048 TR IND	71.81.11.2
89.10.20.1	RC 1 TR IND	71.81.12.2
89.10.20.1	RC 2 TR IND	71.81.12.2
89.10.20.1	RC 4 TR IND	71.81.12.2
89.10.20.1	RC 8 TR IND	71.81.12.2
89.20.20.1	WC 1 TR IND	71.81.12.2
89.20.20.1	WC 2 TR IND	71.81.12.2
89.20.20.1	WC 4 TR IND	71.81.12.2
89.20.20.1	WC 8 TR IND	71.81.12.2
89.40.52.1	R-W REG C BIT IND	71.81.11.2
89.40.51.1	R-W REG A BIT IND	71.81.11.2
89.40.51.1	R-W REG B BIT IND	71.81.11.2
89.40.51.1	R-W REG 8 BIT IND	71.81.11.2
89.40.50.1	R-W REG 4 BIT IND	71.81.11.2
89.40.50.1	R-W REG 2 BIT IND	71.81.11.2
89.40.50.1	R-W REG 1 BIT IND	71.81.11.2
89.40.23.1	A REG C BIT IND	71.81.11.2
89.40.22.1	A REG A BIT IND	71.81.11.2
89.40.22.1	A REG 8 BIT IND	71.81.11.2
89.40.21.1	A REG 8 BIT IND	71.81.11.2
89.40.21.1	A REG 4 BIT IND	71.81.11.2
89.40.20.1	A REG 2 BIT IND	71.81.11.2
89.40.20.1	A REG 1 BIT IND	71.81.11.2
89.40.23.1	B REG C BIT IND	71.81.11.2
89.40.22.1	B REG A BIT IND	71.81.11.2
89.40.22.1	B REG 8 BIT IND	71.81.11.2
89.40.21.1	B REG 8 BIT IND	71.81.11.2
89.40.21.1	B REG 4 BIT IND	71.81.11.2
89.40.21.1	B REG 2 BIT IND	71.81.11.2
89.40.20.1	B REG 1 BIT IND	71.81.11.2
89.50.50.1	ERROR LATCH IND	71.81.12.2
89.50.51.1	A REG VRC ERROR IND	71.81.12.2
89.50.50.1	R-W REG VRC ERR IND	71.81.12.2
89.50.30.1	COMPARE ERR IND	71.81.12.2
89.50.51.1	SKEW ERROR IND	71.81.12.2
89.50.50.1	ECHO ERROR IND	71.81.12.2
89.40.61.1	CHECK CHAR IND	71.81.12.2
89.40.61.1	ODD RED CALL IND	71.81.11.2
89.50.50.1	NO ECHO IND	71.81.11.2
89.60.02.1	LOAD POINT IND	71.81.11.2
89.60.02.1	REWIND IND	71.81.12.2
89.60.02.1	REWIND AND UNLOAD IND	71.81.12.2
89.60.11.1	GO IND	71.81.11.2
89.60.20.1	READ COND IND	71.81.12.2
89.60.20.1	READ ONLY IND	71.81.12.2
89.60.30.1	WR COND IND	71.81.12.2
89.60.31.1	WR TR REL IND	71.81.12.2
89.60.30.1	WRITE IND	71.81.12.2
89.60.31.1	DISC IND	71.81.12.2
89.60.31.1	WR TR IND	71.81.12.2
89.60.31.1	ERASE IND	71.81.12.2
89.60.40.1	BKSP IND	71.81.12.2
89.60.40.1	BAC JARD IND	71.81.12.2
89.30.10.1	READ-DELAY IND	71.81.12.2
89.30.10.1	RDD IND	71.81.12.2
89.30.11.1	MDD IND	71.81.12.2
89.30.11.1	WRITE DELAY IND	71.81.12.2
89.30.56.1	FWD STOP DELAY IND	71.81.11.2
89.50.41.1	LRCR C BIT IND	71.81.11.2
89.50.41.1	LRCR A BIT IND	71.81.11.2
89.50.41.1	LRCR 8 BIT IND	71.81.11.2
89.50.40.1	LRCR 8 BIT IND	71.81.11.2
89.50.40.1	LRCR 4 BIT IND	71.81.11.2
89.50.40.1	LRCR 2 BIT IND	71.81.11.2
89.50.40.1	LRCR 1 BIT IND	71.81.11.2
89.40.60.1	FIRST CHAR IND	71.81.12.2

\* ALL INDICATOR SIGNALS ARE 18 LINES

TAPE 12.000 DATE 17.4.62  
TA 3733A TA 1282

DATE	EC NO	DATE	EC NO	DATE	EC NO
1-25-62	110432.F	1-25-62	112035		