

0724300

WRITE GATING INPUT TO TAU

1401 MT

71.51.21.2

TAG DATE

+U DATA C
70.51.21.2-#1

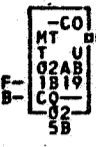
-T MANUAL C BIT
71.71.31.2-#2



+U C BIT WRITE
DATA LINE
#G-B9.02.30.1

+U DATA A
70.51.21.2-#3

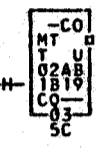
-T MANUAL A BIT
71.71.31.2-#4



+U A BIT WRITE
DATA LINE
#H-B9.02.30.0

+U DATA B
70.51.21.2-#5

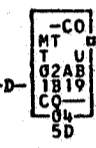
-T MANUAL B BIT
71.71.31.2-#6



+U B BIT WRITE
DATA LINE
#J-B9.02.30.0

+U DATA 8
70.51.21.2-#7

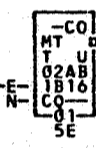
-T MANUAL 8 BIT
71.71.31.2-#8



+U 8 BIT WRITE
DATA LINE
#K-B9.02.30.0

+U DATA 4
70.51.21.2-#9

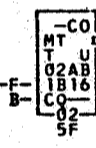
-T MANUAL 4 BIT
71.71.31.2-#A



+U 4 BIT WRITE
DATA LINE
#L-B9.02.30.0

+U DATA 2
70.51.21.2-#B

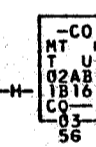
-T MANUAL 2 BIT
71.71.31.2-#C



+U 2 BIT WRITE
DATA LINE
#M-B9.02.30.0

+U DATA 1
70.51.21.2-#D

-T MANUAL 1 BIT
71.71.31.2-#E



+U 1 BIT WRITE
DATA LINE
#N-B9.02.30.0

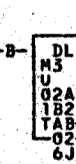
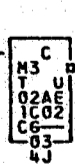
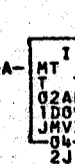
-T TURN OFF TI
71.71.21.2

+U EOP TEST
70.11.41.2-#F



+U TURN OFF TI
#P-B9.02.30.0

-T PROCESS M
TAPE TAU 9
99.99.91.1



-N PROCESS M
#Q-B9.02.25.0

SHIELD GRD
#R-00.00.00.0

SHIELD GRD
#S-00.00.00.0

-N PROCESS M
#T-B9.02.26.0

- *1-02A1D25J
- *2-02A1D23D
- *3-02A1D256
- *4-02A1D23F
- *5-02A1D25H
- *6-02A1D23E
- *7-02A1D25F
- *8-02A1D23G
- *9-02A1D25E
- *A-02A1D23H
- *B-02A1D25D
- *C-02A1D23J
- *D-02A1D25C
- *E-02A1D23K
- *F-02A1C26N
- *G-02A1A24G
- *H-02A1A24E
- *I-02A1D25K
- *J-02A1A24F
- *K-02A1A24D
- *L-02A1A24C
- *M-02A1A24B
- *N-02A1A24A
- *O-02A1B246
- *P-02A1B25K
- *Q-02A1B246
- *R-02A1B24H
- *S-02A1C24H
- *T-02A1C246
- *U-02TC-049
- *V-02TC-049

724300
604A

COMMENTS
SIGNALS PRECEDED BY * INDICATE
DIRECT CABLE EXPANSION

TAG DATE E.C.NO.
A 06-12-61 110320Z
E 09-16-63 118575B

TAG DATE E.C.NO.
B 12-06-61 113422

TAG DATE E.C.NO.
C 04-13-62 115280

TAG DATE E.C.NO.
D 12-17-62 116749E